



Docket No.: 042390.P6725

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 25 2001

Technology Center 2100

In re Application of:

Altug Koker et al.

Application No.: 09/372,296

Filed: August 11, 1999

For: **REDUCING MEMORY ACCESS
LATENCIES FROM A BUS USING
PRE-FETCHING AND CACHING**

Examiner: Jasmine Song

Art Group: 2187

AMENDMENT AND RESPONSE TO THE OFFICE ACTION

BOX NON-FEE AMENDMENT
Assistant Commissioner for Patents
Washington, DC 20231-9998

Sir:

In response to the outstanding Office Action mailed January 19, 2001, please
amend the above-identified Application as follows:

IN THE SPECIFICATION

Please replace the paragraph beginning on page 7, lines 8 through 14, with the
following rewritten paragraph:

--The peripheral devices 160₁ to 160_p are connected to the peripheral bus 140 to
perform peripheral tasks. Examples of peripheral devices include a network interface and a
media interface. The network interface connects to communication channel such as the
Internet. The Internet provides access to on-line service providers, Web browsers, and